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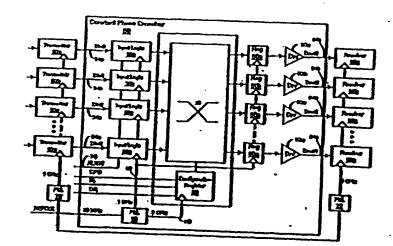
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Poblished

With international search report.

Before the expiration of the time limit for amending the claims and to be republished in the crent of the receipt of amendments.

) THE CONSTANT PHASE CROSSBAR SWITCH



Ibstract

A constant phase crossies switch system (50) which avoids phase discontinuities at the couputs of the crossies switch. The crossies system includes input logic (56a—56n), a crossion switch (58), cusput logic (60a—60n) and a phase locked loop (68). The phase loop is used to generate a high speed internal clock from a system clock. High speed serial data streams transmitted at the internal frequency are received from corresponding transmitters and are coupled to the input logic. The input logic generates multiple versions to specify are received near surresponding nanounally care and confirm to many and the stream, one of the versions being undelayed and the other versions delayed by some fraction of a bit time. State machines boles to seed up terring of the serial spiritual angula and no terring or other in the natural or o to much come market and the serial spiritual s to the high speed internal clock. The selected version of the data stream is employed as the active input to the crossion switch. The data stream is clocked into an output register which is clocked by the internal clock. The selection of the to remain to the strial data stream in the destribed manner avoids phose discontinuities upon switching of someting transmitters.